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NEW QUESTION: 1

クライアントがプロジェクトの終了を検討していると言ったとき、チームは通信システムの実装に取り組み組んでいます。プロジェクトマネージャーは、プロジェクトが危険にさらされている理由を理解するために、クライアントとの緊急会議をスケジュールします。プロジェクトマネージャーがプロジェクトをアクティブに保つというクライアントの決定に影響を与えるために使用できる2つのツールまたは手法はどれですか。(2つ選択してください)

- A. 独裁的な意思決定
- B. 費用便益分析
- C. 代替分析
- D. 多基準意思決定
- E. 投票

Answer: B,D

NEW QUESTION: 2

When can an IBM Tivoli Identity Manager (Tivoli Identity Manager) functional test case be executed on a Tivoli Identity Manager adapter?

- A. after test cases on the Tivoli Identity Manager server configuration have been completed
- B. when a remediation procedure exists as part of the risk assessment if the test case fails
- C. after the adapter is installed and the corresponding service has been reconciled
- D. after performance tests on the adapter have been completed

Answer: A

NEW QUESTION: 3

Which two statements about EIGRP load balancing are true? (Choose two)

- A. A path can be used for load balancing only if it is a feasible successor
- B. EIGRP supports unequal-cost paths by default
- C. EIGRP supports 6 unequal-cost paths
- D. Cisco Express Forwarding is required to load-balance across interfaces
- E. Any path in the EIGRP topology table can be used for unequal-cost load balancing

Answer: A,C

Explanation:

EIGRP provides a mechanism to load balance over unequal cost paths (or called unequal cost load

balancing) through the "variance" command. In other words, EIGRP will install all paths with metric < variance * best metric into the local routing table, provided that it meets the feasibility condition to prevent routing loop. The path that meets this requirement is called a feasible successor. If a path is not a feasible successor, it is not used in load balancing.

Note: The feasibility condition states that, the Advertised Distance (AD) of a route must be lower

than the feasible distance of the current successor route.

NEW QUESTION: 4

Which features does the Dual-core Itanium 2 processor offer that were not available in earlier Itanium processors?

A. maximum 9MB Level 3 cache and single thread per core

B. single core and single thread per core

C. Intel Cache Safe Technology and 1.6GHz clock speed

D. 1.6GHz clock speed and single thread per core

Answer: C

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